

# VTG-4108 TTL

video source for mobile display testing



## Flexible Video Signal Source

The VTG-4108 supports the need for testing small sized color and monochrome panels. It combines a flexible and user friendly test signal source and a fully configurable LVDS, TTL and Composite PAL/NTSC output. A wide variety of data signal configurations and timing modes can programmed to the device.

## Supports present and future needs

In addition to the outstanding output signal performance, the VTG-4108 provides superior flexibility by storing timing files, predefined test pattern and custom made bitmaps internally. These features allow the VTG-4108 to support a multitude of different panels and tests.

## Powerful programmability and software support

- USB connection to PC
- Powerfull pattern drawing
- Unlimited number of patterns and timings
- Bitmap support
- Configurable TTL output pin assignment

## Applications include:

- Manufacturing testing
- Engineering signal source
- QA source of standard signals
- Servicing

# VTG-4108 TTL

video source for mobile display testing



## Specifications

### Display Memory

2048 x 1024 x 8 bit colors  
out of 16.7 million, true color

### Pixel Frequency

1 to 80 Mpix/s, step 0,01 MHz,  
accuracy +50 ppm

### Horizontal Timing

Scan Range 1 - 1000 kHz  
Period 256 - 2048 pixels  
Sync Pulse 2 - 1024 pixels  
Back Porch 0 - 512 pixels  
Display Resolution 16 - 1024 pixels, active  
Adjust Step 1 pixel for all dot clocks

### Vertical Timing

Scan Range 1 to 1000 Hz  
Period 4 to 2000 lines  
Sync Pulse 1 to 1024 lines  
Back Porch 0 to 1024 lines  
Display Resolution 1 to 1024 lines, active  
Adjust Step 1 line for all parameters

### LVDS Output

Format: 21 bit serializer w 18 bit video.  
Configurable output data mapping.  
Transmitter: Maxim 9209 with 4 LVDS pairs.  
Clock range of 8 MHz to 54 MHz.  
Connector: 26 pin MDR connector with DISM  
pin-out configuration.

### TTL Output

Format: Parallel TTL output selectable for 3.3V (LVCMOS) or 5V (CMOS) levels.  
Selectable number of pixels per clock.  
Data output: 3 x 8 bits of video, Hsync (+/-), Vsync (+/-), Blank/Enable (+/-) and pixel clock (data on falling/rising/both) Grounds and power on fixed pins.  
All other signals are software routed to any connection/order.  
Power supply: 12 Vdc (0.5 A) and 3.3 / 5 Vdc (1 A)  
Connector: Dual in-line 2.54 pitch 50 pin male header.

### System Requirement

Windows™ XP

### Power

+12 VDC / 1 A  
(External AC/DC adapter included)

### Dimensions

390 x 140 x 54 mm

### Data Communication

USB 2.0 compatible

### SW Support:

WinVTG User Interface  
Windows DLL software library  
Visual Basic and C++ sample programs

All specifications are subject to change



[www.unigraf.fi](http://www.unigraf.fi)

**UNIGRAF OY** Ruukintie 3, FI-02330 Espoo, Finland  
Phone +358 9 859 550, fax +358 9 802 6699

**UNIGRAF-USA** Phone +1 888 362 7960, fax +1 605 362 7961,  
[www.unigraf-us.com](http://www.unigraf-us.com)

Please visit [www.unigraf.fi](http://www.unigraf.fi) for listing of Unigraf Worldwide Distribution

